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PATENT

Practitioner's Docket No. MI22-2296

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Garo J. Derderian et al.

Application No.: 10/671,922  
Filed: 09/24/03Group No.: 1762  
Examiner: Unassigned

For: Atomic Layer Deposition Methods, and Methods of Forming Materials Over Semiconductor Substrates

Commissioner for Patents  
Washington, D.C. 20231

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Robin Saldivia

  
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application Serial No. ....10/671,922  
Filing Date ..... September 24, 2003  
Inventor..... Garo J. Derderian et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit..... 1762  
Examiner ..... Unassigned  
Attorney's Docket No. ....MI22-2296  
Customer No. ....021567

Title: Atomic Layer Deposition Methods, and Methods of Forming Materials Over  
Semiconductor Substrates

**INFORMATION DISCLOSURE STATEMENT**

References - - See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patents and other references listed on the attached Form PTO-1449. No admission is made regarding whether all the submitted references are prior art.

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Citation of these references is respectfully requested.

Respectfully submitted,

Date: 9/8/04



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Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-2296		SERIAL NO. 10/671,922	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Goro J. Dorderian et al.			
				FILING DATE September 24, 2003		GROUP 1762	
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
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	AB	6,022,595	02/06/00	McDonald et al.			
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	AC	WQ 02/27063 A2	04/04/02	PCT			Yes No
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	AL						
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